

Fig. 4a

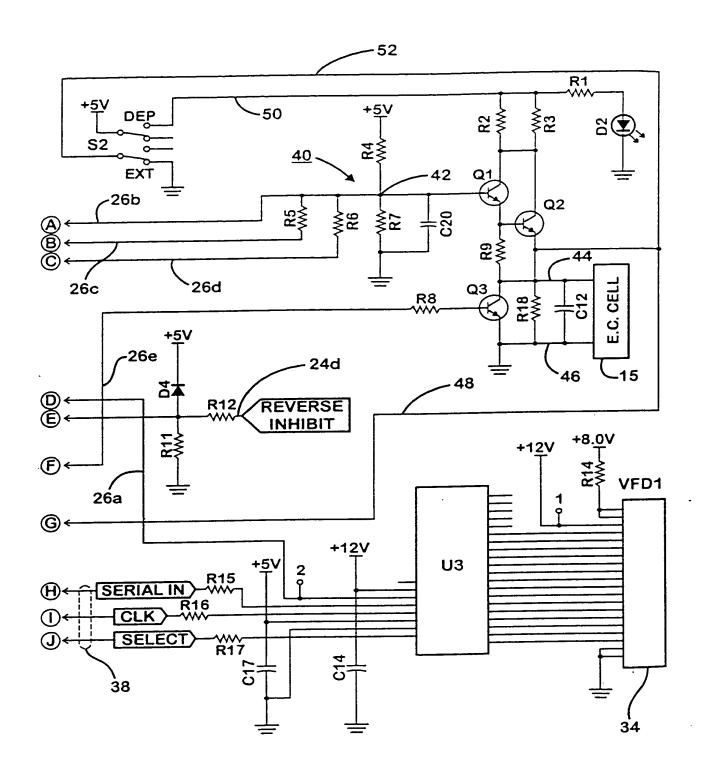
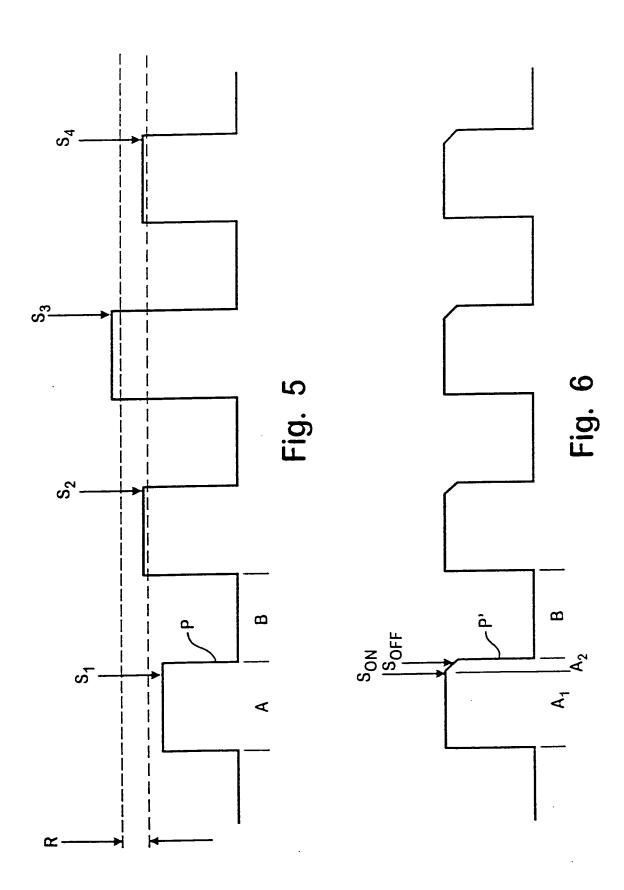
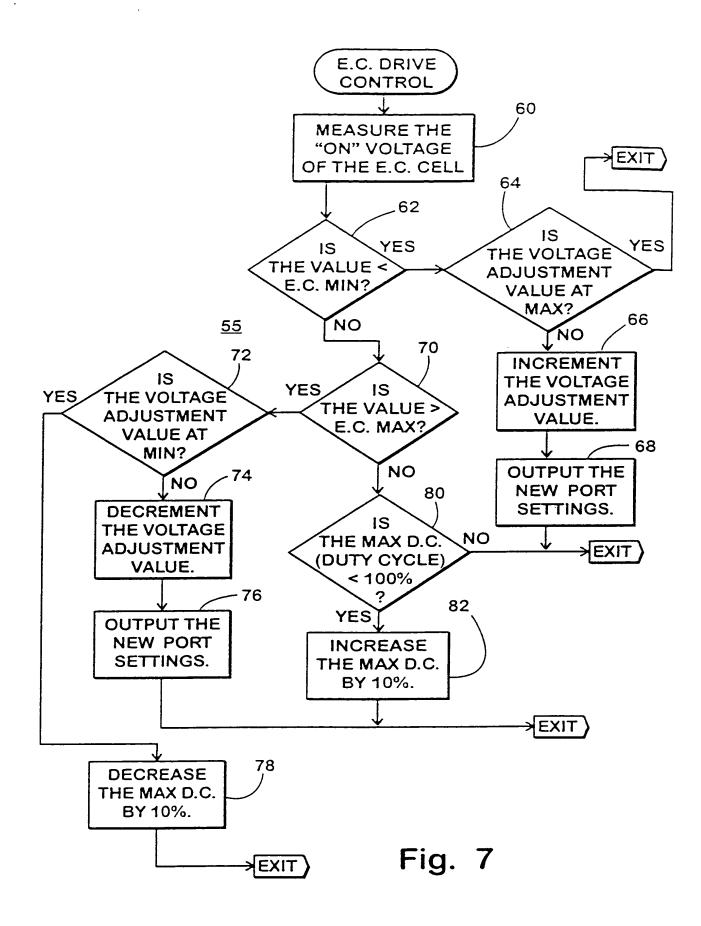


Fig. 4b





PORT SETTINGS

STEP No.	MSB	LSB
8	1	1
7	1	Hi Z
6	1	0
5 4	Hi Z	1
4	Hi Z	Hi Z
3	Hi Z	0
2	0	1
1	0	Hi Z
0	0	0

Fig. 8

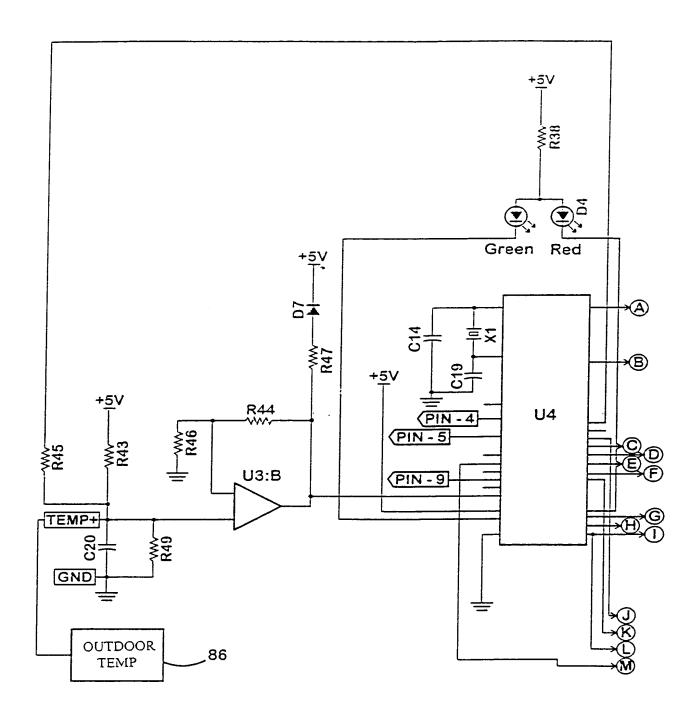


Fig. 9a

